

<b>Notic of References Cited</b>	Application/Control No. 10/084,145		Applicant(s)/Patent Under Reexamination NIGHTINGALE, ANDREW MARK	
	Examiner Toan M Le		Art Unit 2863	Page 1 of 1

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
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X	B	US-6,581,019	06-2003	Bapst et al.	702/120
X	C	US-6,574,691	06-2003	Jirgal et al.	710/100
X	D	US-6,425,071	07-2002	Lo et al.	712/41
X	E	US-6,678,645	01-2004	Rajsuman et al.	703/20
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	J	US-			
	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
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**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)			
X	U	Kishi et al., "Hardware Development of micro-Plat", December 2000, OKI Technical Review 184, Vol. 67, Pages 24-27			
	V	IBM, "The CoreConnect Bus Architecture", IBM 1999			
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\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.